



Docket No.: 209665US-2

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

RE: Application Serial No.: 09/876,019

Applicants: Seiichi MORI

Filing Date: June 8, 2001

For: SEMICONDUCTOR MEMORY INTEGRATED
CIRCUIT AND ITS MANUFACTURING METHOD

Group Art Unit: 2814

Examiner: Hoai V. PHAM

SIR:

Attached hereto for filing are the following papers:

**RESPONSE TO RESTRICTION REQUIREMENT
INFORMATION DISCLOSURE STATEMENT
LIST OF RELATED CASES
CITED PENDING APPLICATIONS (2)
REQUEST FOR EXTENSION OF TIME (1 MONTH)**

Our check in the amount of \$110.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time.

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Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

Gregory J. Maier

Registration No. 25,599



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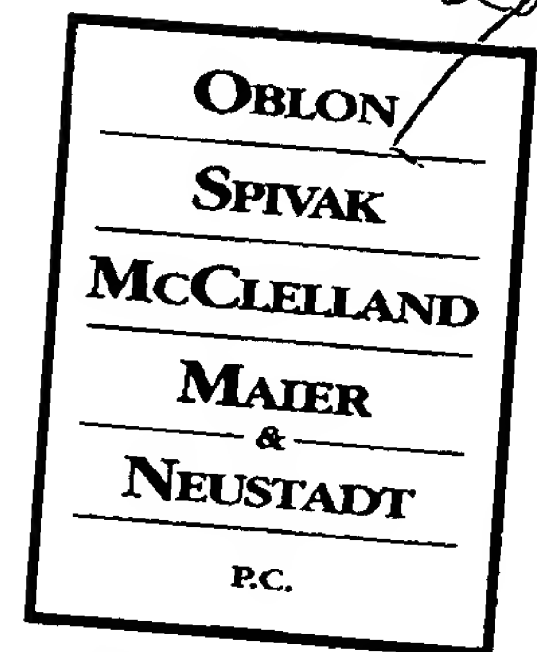
(703) 413-3000 (phone)
(703) 413-2220 (fax)

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Carl E. Schlier

Registration No. 34,426



ATTORNEYS AT LAW

GREGORY J. MAIER
(703) 413-3000
GMAIER@OBLON.COM

CARL E. SCHLIER
(703) 413-3000
CSCHLIER@OBLON.COM

TECHNOLOGY CENTER 2800

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